

IN THE CLAIMS:

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Please cancel claim 2 and amend claims 1, 5, 10-14, 17, 21, 23, 27 and 30 as follows.

1. (Currently Amended) A network switch, comprising:
a data port for communicating with a data network;
a statistics counter connected to said data port for monitoring operational parameters associated with the data port, said statistics counter including statistics registers therein; and
a statistics gathering circuit connected to said statistics counter for reading the statistics registers, and for directly transmitting data from the statistics registers to a remote system memory; and
direct memory access circuitry, wherein the statistics gathering circuit transmits the data from the statistics registers to the remote system memory via a Direct Memory Access (DMA) operation.

2. (Cancelled)

3. (Original) A network switch as recited in claim 1, further comprising a timer unit connected to said statistics gathering circuit for controlling said statistics gathering circuit to repeatedly read the statistics registers and repeatedly transmit the data from the statistics registers to the remote system memory after a predetermined period of time.

4. (Original) A network switch as recited in claim 3, wherein said timer unit comprises a register containing a timer value indicating a number of system clock cycles which determines the predetermined period.

5. (Currently Amended) A network switch as recited in claim 1, further comprising a CPU interface unit for interfacing the network switch to ~~the~~ a remote CPU, said CPU interface unit comprising the statistics gathering circuit therein.

6. (Original) A network switch as recited in claim 5, wherein said CPU interface unit also comprises the statistics counter therein.

7. (Original) A network switch as recited in claim 1, said network switch comprising a communications channel therein, and wherein information is communicated from the data port to the statistics counter via the communication channel.

8. (Original) A network switch as recited in claim 7, wherein said statistics gathering circuit is connected to said statistics counter via the communication channel.

9. (Original) A network switch as recited in claim 1, said network switch comprising a plurality of data ports and a plurality of statistics counters therein, and wherein each data port of said plurality of data ports has at least one individual statistics counter associated therewith.

10. (Currently Amended) A network switch as recited in claim 1, wherein said statistics gathering circuit is configured to transmit the data from the statistics registers to a predetermined section of the remote system memory, said network switch further comprising

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a CPU interface unit which directs a remote CPU to identify where the data for the data port is stored in the predetermined section of remote memory.

11. (Currently Amended) A network switch as ~~recited in claim 1~~, comprising:
a data port for communicating with a data network;
a statistics counter connected to said data port for monitoring operational parameters associated with the data port, said statistics counter including statistics registers therein; and

a statistics gathering circuit connected to said statistics counter for reading the statistics registers, and for directly transmitting data from the statistics registers to a remote system memory; and

further comprising an active counter register which is configured to selectively enable statistics gathering from selected ones of the statistics registers.

12. (Currently Amended) A network switch as ~~recited in claim 9~~, comprising:
a data port for communicating with a data network;

a statistics counter connected to said data port for monitoring operational parameters associated with the data port, said statistics counter including statistics registers therein; and

a statistics gathering circuit connected to said statistics counter for reading the statistics registers, and for directly transmitting data from the statistics registers to a remote system memory; and

said network switch comprising a plurality of data ports and a plurality of statistics counters therein, and wherein each data port of said plurality of data ports has at least one individual statistics counter associated therewith; and

said network switch further comprising an active counter register which is configured to selectively enable statistics gathering by said statistics gathering circuit from selected ones of the plurality of statistics counters.

13. (Currently Amended) A method of monitoring port activity in a network switch, said method comprising the steps of:

storing port activity data in a statistics register on a the network switch;

reading the port activity data with a statistics gathering unit;

transmitting the port activity data directly to a remote system memory, thereby reconstructing the statistics register in the remote system memory; and

accessing the remote system memory with a remote CPU to read the reconstructed statistics register.

14. (Currently Amended) A method as recited in claim 13, wherein said step of transmitting the port activity data comprises transmitting via a Direct Memory Access (DMA) operation.

15. (Original) A method as recited in claim 13, further comprising a step of repeating the reading and transmitting steps at predetermined intervals.

16. (Original) A method as recited in claim 15, wherein said predetermined interval is a predetermined number of system clock cycles.

17. (Currently Amended) A method as recited in claim 13, wherein the step of storing port activity data comprises storing the port activity data in a plurality of statistics registers on a the network switch, wherein the transmitting step comprises transmitting the port activity data to a the remote system memory to thereby reconstruct the plurality of statistics registers in the remote system memory, and wherein the remote CPU accesses the remote system memory to read selected ones of the plurality of statistics registers.

18. (Original) A network switch for processing packets, said network switch comprising:

a data port for transmitting and receiving packets over a data network;

a tag insertion unit for inserting a stack-specific tag into a packet;

a processing unit for processing the packet in a stack of network switches in accordance with tag information in the stack-specific tag;

a removing unit for removing the stack-specific tag from the packet when the packet is being switched to a destination port;

a statistics counter connected to said data port for monitoring operational parameters associated with the data port, said statistics counter including statistics registers therein; and

a statistics gathering circuit connected to said statistics counter for reading the statistics registers, and for directly transmitting data from the statistics registers to a remote system memory.

19. (Original) A network switch as recited in claim 18, wherein said stack-specific tag inserted by said insertion unit includes information relating to at least one of stack count, trunk group information, and mirroring information.

20. (Original) A network switch as recited in claim 18, wherein the processing unit processes the packet by forwarding the packet to a mirroring port in accordance with mirroring information in the stack-specific tag.

21. (Currently Amended) A network switch as recited in claim 18, further comprising direct memory access circuitry, wherein the statistics gathering circuit

transmits the data from the statistics registers to the remote system memory via a Direct Memory Access (DMA) operation.

22. (Original) A network switch as recited in claim 18, further comprising a timer unit connected to said statistics gathering circuit for controlling said statistics gathering circuit to repeatedly read the statistics registers and repeatedly transmit the data from the statistics registers to the remote system memory after a predetermined period of time.

23. (Currently Amended) A network switch as recited in claim 18, further comprising a CPU interface unit for interfacing the network switch to ~~the~~ a remote CPU, said CPU interface unit comprising the statistics gathering circuit therein.

24. (Original) A network switch as recited in claim 23, wherein said CPU interface unit also comprises the statistics counter therein.

25. (Original) A network switch as recited in claim 18, said network switch comprising a plurality of data ports and a plurality of statistics counters therein, wherein each data port of said plurality of data ports has at least one individual statistics counter associated therewith.

26. (Original) A method of handling packets in a network switch, said method comprising the steps of:


inserting a stack-specific tag into an incoming packet;
processing the incoming packet in a stack of network switches in accordance with tag information in the stack-specific tag;
storing port activity data relating to a data port receiving the incoming packet, said port activity data being stored in a statistics register on the network switch;
reading the port activity data with a statistics gathering unit;
transmitting the port activity data directly to a remote system memory, thereby reconstructing the statistics register in the remote system memory;
accessing the remote system memory with a remote CPU to read the reconstructed statistics register.

27. (Currently Amended) A method as recited in claim 26, wherein said step of transmitting the port activity data comprises transmitting via a Direct Memory Access (DMA) operation.

28. (Original) A method as recited in claim 26, further comprising a step of repeating the reading and transmitting steps at predetermined intervals.

29. (Original) A method as recited in claim 28, wherein said predetermined interval is a predetermined number of system clock cycles.

30. (Currently Amended) A method as recited in claim 26, wherein the step of storing port activity data comprises storing the port activity data in a plurality of statistics

 registers on a the network switch, wherein the transmitting step comprises transmitting the port activity data to a the remote system memory to thereby reconstruct the plurality of statistics registers in the remote system memory, and wherein the remote CPU accesses the remote system memory to read selected ones of the plurality of statistics registers.

31. (Original) A method as recited in claim 26, further comprising a step of removing the stack-specific tag.
